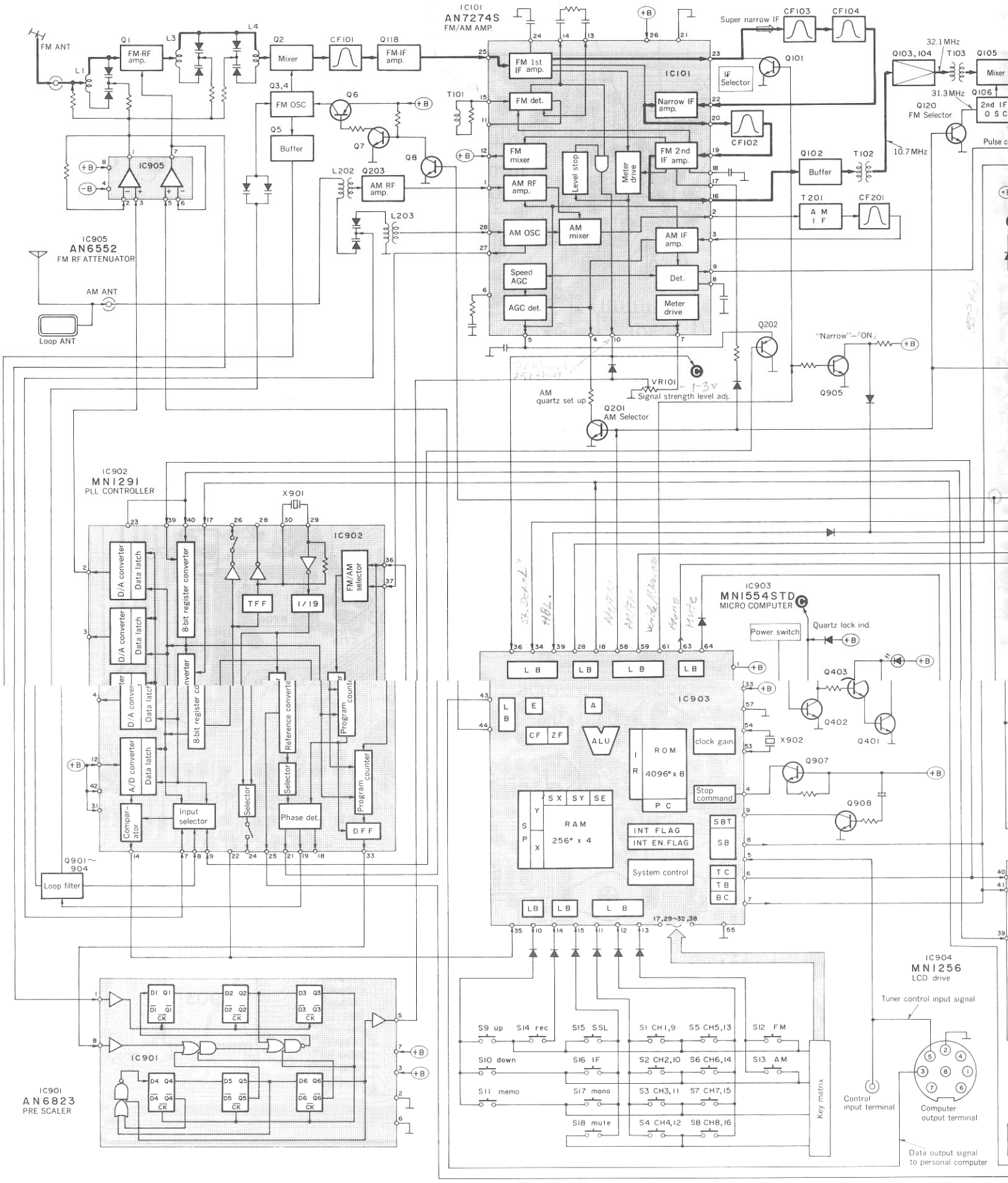
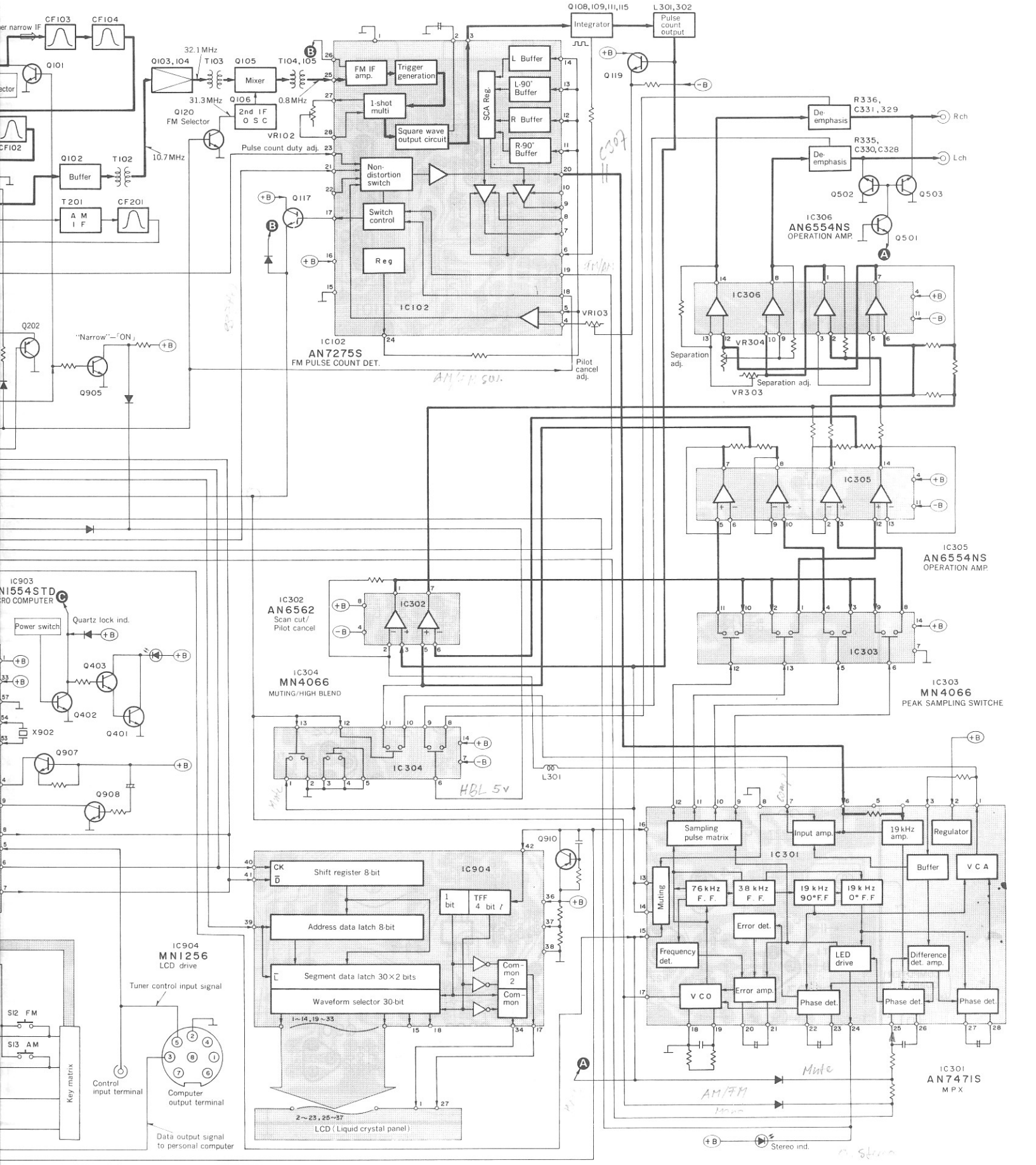
■ BLOCK DIAGRAM




CF103
CF104
Q101
32.1MHz
T103
Q105
Mixer
T104,105
Q103,104
31.3MHz
Q106
2nd IF O S C
0.8MHz
Q120
FM Selector
VR102
Pulse count duty adj.
10.7MHz
Q102
Buffer
T102
T201
A M I F
CF201
Q202
"Narrow"—"ON"
Q905
FM IF amp.
Trigger generation
1-shot multi
Square wave output circuit
Non-distortion switch
Switch control
Reg
SCA Reg.
L Buffer
L-90° Buffer
R Buffer
R-90° Buffer
Q117
IC102
AN7275S
FM PULSE COUNT DET.
VR103
Pilot cancel adj.
Q108,109,111,115
Integrator
L301,302
Pulse count output
Q119
De-emphasis
R336, C331,329
Rch
R335, C330,C328
Lch
Q502
Q503
IC306
AN6554NS
OPERATION AMP.
Q501
Separation adj.
VR304
VR303
IC305
AN6554NS
OPERATION AMP.
IC302
AN6562
Scan cut/ Pilot cancel
IC304
MN4066
MUTING/HIGH BLEND
IC303
MN4066
PEAK SAMPLING SWITCHE
L301
IC903
MN1554STD
MICRO COMPUTER
Power switch
Quartz lock ind.
Q403
Q402
Q401
X902
Q907
Q908
IC904
MN1256
LCD drive
Shift register 8-bit
Address data latch 8-bit
Segment data latch 30×2 bits
Waveform selector 30-bit
Common 2
Common
LCD (Liquid crystal panel)
Tuner control input signal
Control input terminal
Computer output terminal
Key matrix
Data output signal to personal computer
Q910
Sampling pulse matrix
Input amp.
19kHz amp.
Regulator
IC301
Buffer
V C A
Muting
76kHz F. F.
38kHz F. F.
19kHz 90°F.F
19kHz 0° F.F
Error det.
Frequency det.
LED drive
Difference det. amp.
V C O
Error amp.
Phase det.
IC301
AN7471S
MPX
Stereo ind.